

Automatic
Testing
Equipment

LIPS Testing System

PT-9000



Key Feature

Scalable with special LED backlight
Measuring fixture

Scalability up to 16 sets of Output LED
backlighting products

To achieve the goal of rapid detection by
using the standard GPIB interface with the
IEEE-488.2

Easy to edit environment, easy to learn

Scalability the INVERTER and the LIPS
models

Scalability of 1 to 64 lamps of the
INVERTER products and the LIPS models

The INVERTER provides three different
kinds of dimming modes, including voltage,
PWM, and SMBUS

Modular design, easy to maintain

It supports Barcode Reader

It provides statistical reporting capabilities

Application

Motor drive \ UPS \ Chargers \ Power
supplies

Other power products Application
Integration

Accessory

Standard

Power cord	8255 cable
User Manual CD	Fixture flat cable
SCSI cable	PC Link Software

Optional

- Inverter test fixture
- LED driver test fixture
- GPIB cable

Function	POWER	INVERTER
	Inrush Current	Output Power
	Turn On Delay Time	Output Current
	Rise Time (mS)	Output Voltage
	Over Shoot (V) or (%)	Output Frequency
	Hold up Time (mS)	Burst Mode Frequency
	Fall Time	Striking Time
	Dynamic Load (V)	Output Voltage Balance
	Short Protection	Output Current Balance
	Over Current Protection	Open Lamp Protection
	Recovering (V)	DUTY
	Over Voltage Protection (V)	Efficiency
	Regulation (V)	Lin(A)
	Line Regulation	PF
	Ripple & Noise(mVp-p)	Linpk+(A)
	Combine Test (V)	Linpk-A)
Pin (W)	Vin (V)	

Specification	Fixture	Inverter / LED Backlight Driver Test Fixtures
	Measuring output array	16 - 32 sets Lamp
	Measuring input array	1 set
	DC Power Supply	35VDC / 10A (Expansibile)
	AC Power Supply	300V / 1kVA
	Bar Code	Settable
	Fixture Compensation	Software Compensation

Specification

Control Power Supply	
Regulated Light Power Supply	10VDC / 200mA (Max.)
Spare Power Supply -1, -2	
PWM dimming -1	60Hz - 1kHz / Step 1Hz , Duty 1 - 99% / Step 1%
PWM dimming -2	60Hz - 100kHz / Step 1Hz , Duty 1 - 99% / Step 1%
SMBus Dimming	Control DALLAS of DS1803 , Data (Series) , Clock (TTL)
Output Current	1mA - 10mA resolution 10 μ A
Output Voltage	100Vrms - 3000Vrms resolution 1Vrms
Output Frequency	10kHz - 100kHz resolution 10Hz
Burst Mode Frequency	100Hz - 2kHz resolution 1Hz
Burst Mode Duty	1% - 99%
Lighting Protection Time	0 - 5 Sec resolution 0.01Sec
Lamp Current Balance	1. The Max. difference value of subtracting from each other 2. The difference value of subtracting from the Max. to the Min. 3. The Max. difference value with the standard value
High / Low Voltage Protection	Starting voltage point of the test protection
Short Circuit Protection	Confirm whether action or not
Efficiency	0 - 100%
Total Input Voltage	600V (Max.) DC and 10Hz to 100kHz
Total Input Electric Current	20A (Max.) DC and 10Hz to 100kHz
Positive / Negative Peak Value of Input Electric Current	\pm 60A (Max.) 10Hz - 100kHz
Adapter Efficiency	0 - 100%
P.F.	0 - 1
Inrush Current	50A Max.
Timing measurement	0 - 2 Sec resolution 0.01mS
Dynamic Load	THIGH & TLOW 50 μ Sec to 9.999 Sec (DC LOAD)
Load Regulation	DC Load Model
Line Regulation	3310 : 60V / 30A (Max.)
Combine Test	3312 : 30V / 10A (Max.)